

Applicant(s): FUKUDA et al.

Atty. Dkt.: 1-122

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Title:

SEMICONDUCTOR SWITCHING

ELEMENT DRIVING CIRCUIT

Date: March 29, 2001

Washington, D.C. 20231

Assistant Commissioner for Patents

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, the references listed on the attached Form PTO-1449 are being submitted for consideration by the Examiner without any admission that they constitute statutory prior art, or without any admission that they contain subject matter that anticipates the invention or renders the invention obvious to a person of ordinary skill in the art.

The Examiner is requested to initial the attached PTO Form-1449 and to return a copy of same to the undersigned attorney as proof that the listed references have been considered and made of record.

Respectfully submitted,

David G. Posz Reg. No. 37,701

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